

REMARKS/ARGUMENTS

Claims 1, 2 and 5 through 7 remain in this application. Claims 3 and 4 are canceled without prejudice or disclaimer. Claims 5 through 7 are allowed, and claims 1 and 2 are pending and hereby amended.

The drawings are objected to under 35 U.S.C. §132(a) for including new matter. In response, the drawings are hereby amended to correct an innocuous, typographical error, namely element 102 in FIG. 2 is identified as "PC USB HOST" to be consistent with the drawings as originally filed. Reconsideration and withdrawal of the above objection to the drawings are respectfully requested.

Claim 1 is rejected under 35 U.S.C. §103(a) as being anticipated by U.S. Patent Application Publication No. 2004/0157638 to Moran, et al. ("Moran, et al. publication") in view of Universal Serial Bus Specification, Revision 2.0, April 27, 2000 ("USB Specification"). Claim 2 is rejected under 35 U.S.C. §103(a) as being unpatentable over the Moran, et al. publication in view of the USB Specification and U.S. Patent No. 6,654,841 to Lin patent ("Lin patent").

Claim 1 as amended provides, *inter alia*, that the first processor has normally open logical service switches, in which services are unavailable to the second processor during bus enumeration from the first processor to the second processor while inter-processor communication services continue between the processors. Support for the above recitation is provided at page 8, lines 6 through 12, of the specification. It should be noted that this feature of

claim 1 is directed to one of the issues identified at page 2, lines 2 through 4, of the Background of the Invention.

In contrast, the Moran, et al. publication, the USB Specification and the Lin patent do not describe or suggest switches that cause services to be unavailable to the second processor during bus enumeration between processors while inter-processor communication services continue between the processors, as required by amended claim 1. Claim 1 is directed to switches that manage available services for a dual processor architecture having a physical USB link without causing a breakdown or loss of communication in the USB link and re-enumeration between processors. On the other hand, in existing USB enumeration, the old configuration set is removed, thus breaking the communication link, in order to set a new configuration set. Therefore, claim 1 as amended distinguishes patentably from the Moran, et al. publication, the USB Specification, the Lin patent and the suggested combination of these references.

Claim 2 depends from and includes all limitations of independent claim 1 as amended. Therefore, claim 2 distinguishes patentably from the Moran, et al. publication, the USB Specification, the Lin patent and the suggested combination of these references for the reasons stated above for claim 1.

In view of the above, reconsideration and withdrawal of the 35 U.S.C. §103(a) rejections of claims 1 and 2 are respectfully requested.

CONCLUSION

No amendment made was related to the statutory requirements of patentability unless expressly stated herein. Also, no amendment made was for the purpose of narrowing the scope of any claim, unless Applicant has argued herein that such amendment was made to distinguish over a particular reference or combination of references.

The Commissioner is hereby authorized to deduct any additional fees arising as a result of this response, including any fees for Extensions of Time, or any other communication from or to credit any overpayments to Deposit Account No. 50-2117.

It is submitted that the claims clearly define the invention, are supported by the specification and drawings, and are in a condition for allowance. Applicant respectfully requests that a timely Notice of Allowance be issued in this case. Should the Examiner have any questions or concerns that may expedite prosecution of the present application, the Examiner is encouraged to telephone the undersigned.

Respectfully submitted,
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